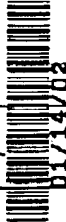


<b>Form PTO-1449</b>  <b>LIST OF PRIOR ART CITED BY APPLICANT</b> <i>(Use several sheets if necessary)</i>		ATTORNEY DOCKET NO. <b>AUS920010717US1</b>		SERIAL NO.  <div style="text-align: right;">           JCS57 U.S. PTO            10/045337              01/14/02         </div>	
		APPLICANT <b>DeWitt et al.</b>			
		FILING DATE		GROUP ART UNIT	

U.S. PATENT DOCUMENTS					
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE
DJH	3,659,272	04/25/1972	Price et al.	340/172.5	
DJH	5,003,462	03/26/1991	Blaner et al.	364/200	
DJH	5,594,864	01/14/1997	Trauben	395/183.15	
DJH	5,632,028	05/20/1997	Thusoo et al.	395/500	
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FOREIGN PATENT DOCUMENTS					
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES NO

OTHER PRIOR ART (including author, title, date, pertinent page, etc.)	
DJH	✓ AIX Version 4.3 Kernel and Subsystems Technical Reference, Vol. 1, "pinu Kernel Service", IBM Corp., 1997.
DJH	✓ Itanium™ Processor Microarchitecture Reference for Software Optimization, Intel Corp., 03/2000, pp. 1-28.
DJH	✓ Itanium™ IA-64 Architecture Software Developer's Manual, Vol. 4: Itanium™ Processor Programmer's Guide, Revision 1.1, Intel Corp., 07/2000, pp. 6-1 to 6-32.
DJH	✓ Itanium™ IA-64 Architecture Software Developer's Manual, Vol. 1: IA-64 Application Architecture, Revision 1.1, Intel Corp., 07/2000, pp. 2-1 to 2-8, 3-1 to 3-20, 4-1 to 4-35, 9-1 to 9-16, 10-1 to 10-16.
DJH	✓ Itanium™ IA-64 Architecture Software Developer's Manual, Vol. 2: IA-64 System Architecture, Revision 1.1, Intel Corp., 07/2000, pp. 2-1 to 2-4, 3-1 to 3-22, 5-1 to 5-36, 7-1 to 7-14.

DATE CONSIDERED 9-21-04	EXAMINER David J. Hunsie
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			APPLICANT <b>DeWitt et al.</b>			
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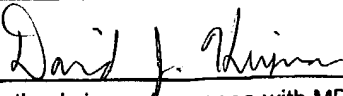
  

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